

PATENT APPLICATION
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q83944

Hiroto OHTAKE, et al.

Appln. No.: 10/509,898

Group Art Unit: 2812

Confirmation No.: 2975

Examiner: Cheung LEE

Filed: October 01, 2004

For: SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated February 3, 2006, in which the Examiner has required a restriction, Applicants respond by electing Group I, *i.e.*, claims 1-4, drawn to a semiconductor device. This election is made without prejudice to the filing of a divisional application directed to the non-elected claims.

Prompt and favorable action on the elected claims is now respectfully requested.

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Respectfully submitted,

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Date: March 3, 2006